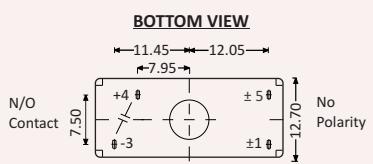
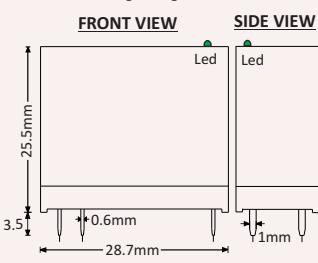
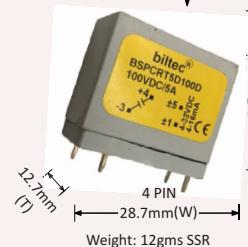




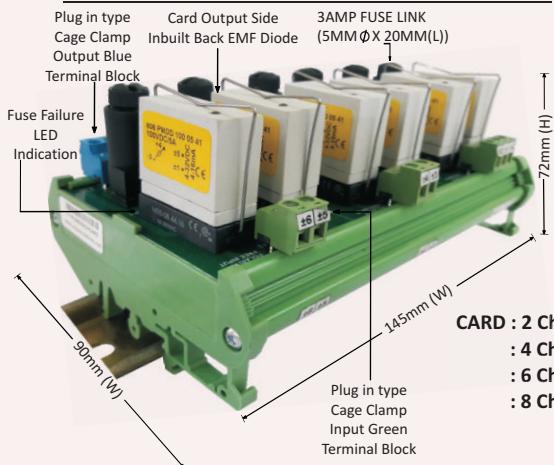
SOLID STATE RELAY

ISO 9001:2015 & ISO 14001:2015 CERTIFIED by InterConformity GmbH

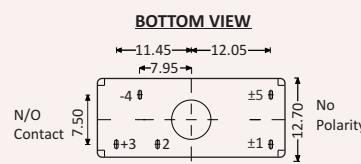
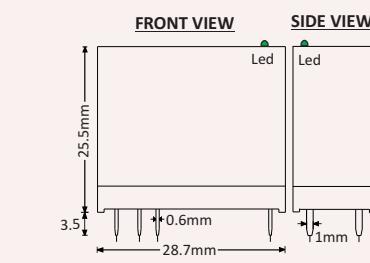
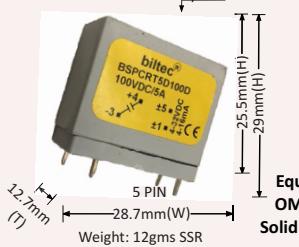
5AMP-MODEL BSPCTRSD100D
BSC806PMDD1000541 I/P. LED



MODEL BTC 806 INTERFACE RELAY CARD - 6 CHANNEL



SAMP-MODEL SPRCTRSD100D-2
BSC806PMDD1000551 I/P. LED



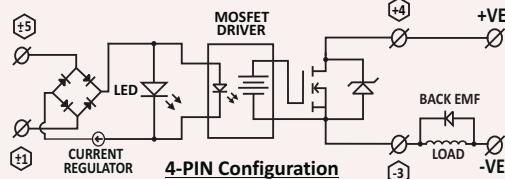
Equivalent to
OMRON - O/E/N
Mechanical RELAY G2R1/58.

Equivalent to
OMRON/FUJI
Solid State RELAY

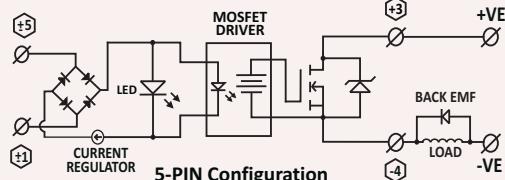
OUTPUT DC CONTROL
COOL POWER MOSFET
CONTROL TECHNOLOGY

- Switching Speeds are Inherently Faster.
- No need to De-rate Power Handling Capacity.
- Input LED Indication
- Ultra Low on State Resistance
- Ultra Low Output Leakage Current
- Isolated Drivers
- N/O Configurations only.
- Reverse Polarity Free Input

DC to DC - Circuit Diagram



DC to DC - Circuit Diagram



- * Across the load back emfdiode is must for high ON/OFF



Difference Between Power MOSFET & Transistor

Power MOSFET	Transistor
High Speed ON/OFF (High Frequency)	Low Speed ON/OFF
High Power Capacity	Low Power Capacity
Voltage Drop Across Power MOSFET is Less	Voltage Drop Across Transistor is more
Size of Heat sink less	Size of Heat sink is more

Output Technical Specifications @ 25°C Unless Specified

Parameters	Symbol	Unit	100V / 05Amp
Output Circuit - Switching Element			POWER MOSFET
Operating Output DC Voltage Range	V	VDC	5-100
Continuos Drain Current @ 20°C	I _D	Amp	5
Continuos Drain Current @ 55°C	I _D	Amp	2.5
Maximum Drain To Source Break Down Voltage	V _{(BR)DSS}	VDC	100
Static Drain To Source ON Resistance	R _{DS(ON)}	Ohm	0.03
On state Voltage Drop @ Rated Current	V _{DSS}	VDC	< 0.7
Power Dissipation	P _D	W	150
Required minimum LOAD current	mA _{DC}	mA	3
Max. OFF State Drain To Source Leakage Current @ V _{GS} =0, T _j =125°C	I _{DSS}	mA	0.2
Pulsed Drain current (less than 60 μS Pulse)	I _{DM}	Amp	100
Output Capacitance	C _{OSS}	pF	300
Peak Diode Recovery dv/dt	dv/dt	V/nS	5
SSR Turn ON Delay Time	T _{D(ON)}	μs	1.8
SSR Turn Off Delay Time	T _{D(OFF)}	μs	< 0.04
Minimum Isolation Resistance between Input Terminals (±5,±1) to Output Terminals (-3,+4) @ 500 VDC	Ω	GΩ	1
Isolation Voltage Input Terminals (±5,±1) to Output Terminals (-3,+4) for 1 Minute	V _{ISO}	kV	2.5
Maximum Junction Temperature	T _{j(max.)}	°C	150 °C
Thermal Resistance R _θ (Junction To Case)	R _{θ(j-c)}	°C/W	0.7

General Specification

Max Barrier Layer Temperature (T _{max})	< 125 °C
Ambient Temperature Range (T _{amb})	0-85 °C
SSR Storage Temperature Range (T _{st})	-40°C to 80°C
Housing Material	UL-94 V0 Grade
SSR Weight	12 grams
Test Standards:	ROHS, IP20
Pending Approvals:	UL 508, VDE, TUV, CSA 22-2 IEC 60947-5-1:2016 IEC 62314:2006

Input Technical Specifications

Parameters	Unit	PMDD/RDD
Control Voltage Range	V	4-32 VDC
Reverse Polarity Protection	-	Polarity Free
Control Supply Current Consumption	mA	4-16 mA
Input Impedance (Current Regulator Circuit Impedance)	Ω	1 kΩ - 2 kΩ
Minimum Turn ON Voltage	VDC	3.5 VDC
Turn OFF Voltage	VDC	< 3.25 VDC
Control Input Status Indication	-	Green LED Indication